

DOUBLE-HIGH MEMORY SYSTEM COMPATIBLE WITH TERMINATION
SCHEMES FOR SINGLE-HIGH MEMORY SYSTEMS

ABSTRACT OF THE INVENTION

A double-high memory system compatible with termination schemes for single-high memory systems. The system includes an interface for input and output of data. A plurality of memory units is configured in two rows. A transmission line couples the plurality of memory units and the interface. The double-high memory system is provided in a non-stacked arrangement.